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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	memistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/03 20:25
L2	0	memistor and nanotechnology	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/03 20:25
L3	3261	nanotechnology	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/03 20:25
L4	87	nanotechnology and (neural adj networks)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/03 20:30
L5	10	nanotechnology and (neural adj networks) and hebbian	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/03 20:25



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Solid-state thin-film **memistor** for electronic neural networks - group of 3 »

S Thakoor, A MOOPENN, T DAUD, AP THAKOOR - Journal of Applied Physics, 1990 - adsabs.harvard.edu

Title: Solid-state thin-film **memistor** for electronic neural networks Authors: Thakoor, S.; Moopenn, A.; Daud, T.; Thakoor, A. P. Affiliation: AA(JPL, Pasadena ...

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[CITATION] Fabrication and Performance of the Solid-state Thin Film **Memistor**

G Shen, J Ou, Z Li - VACUUM SCIENCE AND TECHNOLOGY, 1998 -

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SESSION IX: Functional Components

PR Low - [ieeexplore.ieee.org](#)

... material. The first of these devices, the **memistor**, consists of conductive substrate, insulated connecting leads, and a metallic ...

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VLSI Implementations of Threshold Logic-A Comprehensive Survey - group of 8 »

V Beiu, JM Quintana, MJ Avedillo - IEEE TRANSACTIONS ON NEURAL NETWORKS, 2003 -

[ieeexplore.ieee.org](#)

... They used an electrically adjustable resistor called a **memistor**. Widrow also founded the first neurocomputer hardware company, **Memistor** ...

Cited by 31 - [Web Search](#) - [BL Direct](#)

E1. 4 Digital integrated circuit implementations - group of 7 »

V Beiu - Handbook of neural computation - jaguar.eecs.wsu.edu

... C1.1.3 madaline. They used a type of electronically adjustable resistor called a **memistor**. ... C1.1.4 the first neurocomputer company: the **Memistor** Corporation. ...

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固态薄膜可重编程的模拟可变电阻存储器的制造和性能 - group of 3 »

申功烈, 欧军 - 真空科学与技术, 1998 - 维普资讯

... 韩闹圃旌托阅795 变电阻存储器(**memistor**)构成早期应用的一个

个实例是由液态电介质的电化学元件构成\_J. 1— ...

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Threshold logic implementations: The early days - group of 4 »

V Beiu - Proc. IWANN'03 - jaguar.eecs.wsu.edu

... They used an electrically adjustable resistor called a **memistor**, also founding the first neurocomputer hardware company: **Memistor** Corporation, producing ...

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Die technische Implementierung neuronaler Netzwerke - group of 7 »

R Männer - informatik.uni-mannheim.de

... Das entsprechende Bauelement, der "**Memistor**" (Abb. ... 1.5 in [6]). Abb. 6: Der **Memistor**, ein miniaturisierter variabler Widerstand (Fig. 8.12 aus [6]). ...

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[A survey of perceptron circuit complexity results - group of 5 »](#)

V Beiu - Intl. Joint Conf. Neural Networks IJCNN'03, 2003 - [eecs.wsu.edu](http://eecs.wsu.edu)

... VLSI implementations of TLGs). The earliest examples are Mark I Perceptron, built by Rosenblatt (1957-1958), and Widrow's **memistor** (1960). ...

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[Optimal practical adders using perceptrons - group of 5 »](#)

M Sulieman, V Beiu - Intl. Conf. Neural Networks and Signal Proc. ICNNSP'03, 2003 - [moab.eecs.wsu.edu](http://moab.eecs.wsu.edu)

... Still, TLGs have rarely been used in practice. The earliest examples are Mark I Perceptron, built by Rosenblatt (1957-1958), and Widrow's **memistor** (1960). ...

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Optimal Practical Perceptron Addition Application to Single Electron Technology - group of 2 »

V Beiu, M Sulieman - Proceedings of VLSI-04, 2004 - eecs.wsu.edu

... commercial products. The earliest examples are Rosenblatt's Mark I Perceptron (1957-8), and Widrow's **memistor** (1960). More recently ...[Web Search](#)E1. 4 Digital integrated circuit implementations - group of 7 »

V Beiu - Handbook of neural computation - jaguar.eecs.wsu.edu

... C1.1.3 madaline. They used a type of electronically adjustable resistor called a **memistor**. ... C1.1.4 the first neurocomputer company: the **Memistor** Corporation. ...Cited by 1 - [View as HTML](#) - [Web Search](#)Analog electronic neural network circuits - group of 4 »

HP Graf, LD Jackel - IEEE Circuits and Devices Magazine, 1989 - ieeexplore.ieee.org

Page 1 conn.ctlon from ,l.uosl l 000n.Ctlonu to oth.r Moron. Input P' output Pf.urOf l 44 8755-3996/89,0700-0044\$1 00 © 1989 ...

Cited by 31 - [Web Search](#)DESIGN AND ANALYSIS OF SINGLE-ELECTRON TECHNOLOGY NEURAL-INSPIRED GATES AND ARITHMETIC CIRCUITS

MH SULIEMAN - 2004 - eecs.wsu.edu

... commercial products. The earliest examples are Mark I Perceptron, built by Rosenblatt (1957- 1958), and Widrow's **memistor** (1960). ...Cited by 2 - [Web Search](#)Implementing Neural Hardware With on Chip Training on Field Programmable Gate Arrays

D Braendler - ciscp.bsee.swin.edu.au

Page 1. Implementing Neural Hardware with On Chip Training on Field Programmable Gate Arrays David Braendler A Thesis presented for the ...

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SP Eberhardt, R Tawel, TX Brown, T Daud, AP ... - IEEE Transactions on Industrial Electronics, 1992 - ieeexplore.ieee.org

Page 1 0278-0046/92\$03.00 © 1992 IEEE 552 IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS, VOL. 39, NO.6, DECEMBER 1992 Analog VLSI ...

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**Self-Organizing Maps - group of 9 »**

T Kohonen... - SPRINGER SERIES IN INFORMATION SCIENCES, 1997 - portal.acm.org  
 ... a general class of **Hebbian** models, Neural ... a case study in the **nanotechnology** domain,  
 Proceedings ... architecture, Self-Organizing **neural networks**: recent advances ...  
 Cited by 4925 - [Web Search](#) - [Library Search](#) - [BL Direct](#)

**Neural nets using SET technology - group of 2 »**

E Rouw, R van de Haar, AHM van Roermund, R Klunder ... - Proc. ProRISC/IEEE Workshop Circuits, Systems,  
 and Signal ... - [duteela.et.tudelft.nl](#)  
 ... Adaptations for **Nanotechnology** McCulloch and Pitts neurons ... is impossible when **neural**  
**networks** consist of over a ... learning algorithms like **Hebbian** learning rules ...  
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**Spike Correlation Based Learning For Unsupervised Neural Lattice Structures - group of 2 »**

E Rouw, J Hoekstra, AHM Van Roermund - IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND  
 SYSTEMS, 2001 - [ieeexplore.ieee.org](#)  
 ... Figure 4: Classical conditioning (a) and **Hebbian** learning using a ... One of the  
 similarities between biological **neural networks** and **nanotechnology** is their ...  
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**Spike time/frequency correlation based learning for unsupervised neural lattice structures - group of 3 »**

E Rouw, A van Roermund, J Hoekstra - [stw.nl](#)  
 ... Fig. 5. Classical conditioning (a) and **Hebbian** learning using a ... One of the similarities  
 between biological **neural networks** and **nanotechnology** is their ...  
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**CrossNets: possible neuromorphic networks based on nanoscale components - group of 3 »**

O Tuerel, K Likharev - International Journal of Circuit Theory and Applications, 2003 - [doi.wiley.com](#)  
 ... Neuromorphic (or '**neural**') **networks** [5–10] with their high parallelism and intrinsic  
 redundancy look like a perfect application for SET devices. ...  
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**Neural Networks in Teletraffic Control: Pro et Contra? - group of 2 »**

BD Reljin, IS Reljin - Telecommunications in Modern Satellite, Cable and ..., 1999 - [ieeexplore.ieee.org](#)  
 ... **Neural Networks** in Teletraffic Control: Pro et Contra? ... Keywords - **Neural networks**,  
 teletraffic, communication networks, fractallmultifractal traffic. ...  
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**CrossNets: Neuromorphic Networks for Nanoelectronic Implementation - group of 2 »**

O Tuerel, K Likharev - ICANN, 2003 - [rsfq1.physics.sunysb.edu](#)  
 ... Simulations show that, similarly to the non-**Hebbian** case [8, 9], an ... **Nanotechnology**  
 12 (2001) 91-95 8. Fölling, S., Türel, Ö., Likharev ... on **Neural Networks**. ...  
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**CrossNets: Neuromorphic Networks for Nanoelectronic Implementation**

O Kaynak... - Springer  
 ... Simulations show that, similarly to the non-**Hebbian** case [8, 9 ... **Nanotechnology** 12  
 (2001) 91 – 95 8. Fölling, S., Türel, Ö., Likharev, KK ... on **Neural Networks**. ...

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Quantum coherence in microtubules: A neural basis for emergent consciousness

SR Hameroff - Journal of Consciousness Studies, 1994 - ingentaconnect.com

... helped to lead the way to artificial **neural networks**« ( ANNs«) ✓ parallel ... by a postulated fast (ten to hundreds of milliseconds) **Hebbian** synaptic modulation ...

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[book] Thinking in complexity: the computational dynamics of matter, mind, and mankind - group of 2 »

K Mainzer - 2004 - books.google.com

... **Nanotechnology** with new materials, aswell as articial life and artificial ... The 7th IEEE International Workshop on "Cellular **Neural Networks** and Their ...

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